

CPU | Caches | Mainboard | Memory | SPD | Graphics | About

Processor

Name

Code Name  Brand ID

Package

Technology  Core Voltage



Specification

Family	<input type="text" value="6"/>	Model	<input type="text" value="F"/>	Stepping	<input type="text" value="B"/>
Ext. Family	<input type="text" value="6"/>	Ext. Model	<input type="text" value="F"/>	Revision	<input type="text" value="G0"/>

Instructions

Clocks (Core #0)

Core Speed

Multiplier

Bus Speed

Rated FSB

Cache

L1 Data	<input type="text" value="2 x 32 KBytes"/>	<input type="text" value="8-way"/>
L1 Inst.	<input type="text" value="2 x 32 KBytes"/>	<input type="text" value="8-way"/>
Level 2	<input type="text" value="4096 KBytes"/>	<input type="text" value="16-way"/>
Level 3	<input type="text"/>	<input type="text"/>

Selection

Cores  Threads