HWiNFO32 v3.70-1153

Creation Time	01.03.2011 09:56

MEGG-PC

[Current Computer]

Computer Brand Name:	**MSI MS-7250**

[Operating System]

Operating System:	Microsoft Windows 7 Ultimate Build 7601
Service Pack:	Service Pack 1

Central Processor(s)

[CPU Unit Count]

Number Of Processor Packages (Physical):	**1**
Number Of Processors Cores:	**2**
Number Of Logical Processors:	**2**

AMD Athlon 64 X2 6000+

[General Information]

Processor Name:	**AMD Athlon 64 X2 6000+**
Original Processor Frequency:	**3000.0 MHz**
Original Processor Frequency [MHz]:	**3000**
CPU ID:	00040F33
Extended CPU ID:	00040F33
CPU Brand Name:	AMD Athlon(tm) 64 X2 Dual Core Processor 6000+
CPU Vendor:	AuthenticAMD
CPU Stepping:	JH-F3
CPU Code Name:	Windsor
CPU Platform:	Socket AM2
Number of CPU Cores:	2
Number of Logical CPUs:	2

[Operating Points]

CPU HFM (Maximum):	3000.0 MHz = 15.00 x 200.0 MHz @ 1.4000 V
CPU Current:	3000.0 MHz = 15.00 x 200.0 MHz @ 1.3500 V
CPU Bus Type:	Hyper-Transport v1.02
Maximum Supported Hyper-Transport Link Clock:	1000 MHz

Current Hyper-Transport Link Clock:	1000 MHz

[Cache and TLB]

L1 Cache:	Instruction: 2 x 64 KBytes, Data: 2 x 64 KBytes
L2 Cache:	Integrated: 2 x 1 MBytes
Instruction TLB:	Fully associative, 32 entries
Data TLB:	Fully associative, 32 entries

[Standard Feature Flags]

FPU on Chip	Present
Enhanced Virtual-86 Mode	Present
I/O Breakpoints	Present
Page Size Extensions	Present
Time Stamp Counter	Present
Pentium-style Model Specific Registers	Present
Physical Address Extension	Present
Machine Check Exception	Present
CMPXCHG8B Instruction	Present
APIC On Chip / PGE (AMD)	Present
Fast System Call	Present
Memory Type Range Registers	Present
Page Global Feature	Present
Machine Check Architecture	Present
CMOV Instruction	Present
Page Attribute Table	Present
36-bit Page Size Extensions	Present
Processor Number	Not Present
CLFLUSH Instruction	Present
Debug Trace and EMON Store	Not Present
Internal ACPI Support	Not Present
MMX Technology	Present
Fast FP Save/Restore (IA MMX-2)	Present
Streaming SIMD Extensions	Present
Streaming SIMD Extensions 2	Present
Self-Snoop	Not Present
Multi-Threading Capable	Present
Automatic Clock Control	Not Present
IA-64 Processor	Not Present
Signal Break on FERR	Not Present
Streaming SIMD Extensions 3	Present
PCLMULQDQ Instruction Support	Not Present
MONITOR/MWAIT Support	Not Present
Supplemental Streaming SIMD Extensions 3	Not Present
FMA Extension	Not Present
CMPXCHG16B Support	Present
Streaming SIMD Extensions 4.1	Not Present
Streaming SIMD Extensions 4.2	Not Present
x2APIC	Not Present
POPCNT Instruction	Not Present
AES Cryptography Support	Not Present
XSAVE/XRSTOR/XSETBV/XGETBV Instructions	Not Present
XGETBV/XSETBV OS Enabled	Not Present
AVX Support	Not Present
Half-Precision Convert (CVT16)	Not Present

[Extended Feature Flags]

FPU on Chip	Present
Enhanced Virtual-86 Mode	Present
I/O Breakpoints	Present
Page Size Extensions	Present
Time Stamp Counter	Present
AMD-style Model Specific Registers	Present
Machine Check Exception	Present

CMPXCHG8B Instruction	Present
APIC On Chip	Present
SYSCALL and SYSRET Instructions	Present
Memory Type Range Registers	Present
Page Global Feature	Present
Machine Check Architecture	Present
CMOV Instruction	Present
Page Attribute Table	Present
36-bit Page Size Extensions	Present
Multi-Processing / Brand feature	Not Present
No Execute	Present
MMX Technology	Present
MMX+ Extensions	Present
Fast FP Save/Restore	Present
Fast FP Save/Restore Optimizations	Present
1 GB large page support	Not Present
RDTSCP Instruction	Present
x86-64 Long Mode	Present
3DNow! Technology Extensions	Present
3DNow! Technology	Present
LAHF/SAHF Long Mode Support	Present
Core Multi-Processing Legacy Mode	Present
Secure Virtual Machine	Present
Extended APIC Register Space	Present
LOCK MOV CR0 Support	Present
Advanced Bit Manipulation	Not Present
SSE4A Support	Not Present
Misaligned SSE Mode	Not Present
PREFETCH(W) Support	Not Present
OS Visible Work-around Support	Not Present
Instruction Based Sampling	Not Present
XOP Instruction Support	Not Present
SKINIT, STGI, and DEV Support	Not Present
Watchdog Timer Support	Not Present
TBM0 Instruction Support	Not Present
Lightweight Profiling Support	Not Present
FMA4 Instruction Support	Not Present
Translation Cache Extension	Not Present
NodeId Support	Not Present
Trailing Bit Manipulation	Not Present
Topology Extensions	Not Present
Core Performance Counter Extensions	Not Present
NB Performance Counter Extensions	Not Present

[Enhanced Features]

Core Performance Boost	Not Supported

[Memory Ranges]

Maximum Physical Address Size:	40-bit (1 TBytes)
Maximum Virtual Address Size:	48-bit (256 TBytes)

[MTRRs]

Range 0-80000000 (0MB-2048MB) Type:	Write Back (WB)
Range 80000000-A0000000 (2048MB-2560MB) Type:	Write Back (WB)
Range A0000000-B0000000 (2560MB-2816MB) Type:	Write Back (WB)
Range B0000000-B8000000 (2816MB-2944MB) Type:	Write Back (WB)
Range B8000000-BC000000 (2944MB-3008MB) Type:	Write Back (WB)

Motherboard

[Computer]

Computer Brand Name:	**MSI MS-7250**

[Motherboard]

Motherboard Model:	**MSI MS-7250**
Motherboard Chipset:	**nVidia nForce 570 SLI (MCP55P) + MCP55P(XE)**
Motherboard Slots:	1xPCI, 1xAGP, 4xPCI Express x1, 1xPCI Express x8, 1xPCI Express x16
PCI Express Version Supported:	v1.0
USB Version Supported:	v2.0

[BIOS]

BIOS Manufacturer:	American Megatrends
BIOS Date:	04/07/08
BIOS Version:	V3.10
EFI BIOS	Not Capable
Super-IO/LPC Chip:	Winbond/Nuvoton W83627EHF

ACPI Devices

AMD K8-Prozessor

Device Name:	AMD K8-Prozessor

AMD K8-Prozessor

Device Name:	AMD K8-Prozessor

ACPI-Schalter

Device Name:	ACPI-Schalter

Programmierbarer Interruptcontroller

Device Name:	Programmierbarer Interruptcontroller

[Assigned Resources]

I/O Port: 0020 - 0021
I/O Port: 00A0 - 00A1

[Alternative 1]

I/O Port: 0020 - 0021
I/O Port: 00A0 - 00A1

Systemzeitgeber

Device Name: Systemzeitgeber

[Assigned Resources]

I/O Port: 0040 - 0043
IRQ: 0

[Alternative 1]

I/O Port: 0040 - 0043
IRQ: 0

Hochpräzisionsereigniszeitgeber

Device Name: Hochpräzisionsereigniszeitgeber

[Assigned Resources]

Memory Location: FED00000 - FED00FFF

[Alternative 1]

Memory Location: FED00000 - FED00FFF

DMA-Controller

Device Name: DMA-Controller

[Assigned Resources]

I/O Port: 0000 - 000F
I/O Port: 0081 - 0083
I/O Port: 0087
I/O Port: 0089 - 008B
I/O Port: 008F
I/O Port: 00C0 - 00DF
DMA: 4

[Alternative 1]

I/O Port: 0000 - 000F
I/O Port: 0081 - 0083
I/O Port: 0087
I/O Port: 0089 - 008B
I/O Port: 008F

I/O Port:	00C0 - 00DF
DMA:	4

Standardtastatur (PS/2)

Device Name:	Standardtastatur (PS/2)

[Assigned Resources]

I/O Port:	0060
I/O Port:	0064
IRQ:	1

[Alternative 1]

I/O Port:	0060
I/O Port:	0064
IRQ:	1

Systemlautsprecher

Device Name:	Systemlautsprecher

[Assigned Resources]

I/O Port:	0061

[Alternative 1]

I/O Port:	0061

PCI-Bus

Device Name:	PCI-Bus

[Assigned Resources]

I/O Port:	0000 - 0CF7
I/O Port:	0D00 - FFFF
Memory Location:	000A0000 - 000BFFFF
Memory Location:	000D0000 - 000DFFFF
Memory Location:	BF000000 - FEBFFFFF

[Alternative 1]

I/O Port:	0000 - 0CF7
I/O Port:	0D00 - FFFF
Memory Location:	000A0000 - 000BFFFF
Memory Location:	000D0000 - 000DFFFF
Memory Location:	BF000000 - FEBFFFFF

System CMOS/Echtzeituhr

Device Name: System CMOS/Echtzeituhr

[Assigned Resources]

I/O Port: 0070 - 0071
IRQ: 8

[Alternative 1]

I/O Port: 0070 - 0071
IRQ: 8

Systemplatine

Device Name: Systemplatine

[Assigned Resources]

Memory Location: 00000000 - 0009FFFF
Memory Location: 000C0000 - 000CFFFF
Memory Location: 000E0000 - 000FFFFF
Memory Location: 00100000 - BEFFFFFF
Memory Location: FEC00000 - FFFFFFFF

[Alternative 1]

Memory Location: 00000000 - 0009FFFF
Memory Location: 000C0000 - 000CFFFF
Memory Location: 000E0000 - 000FFFFF
Memory Location: 00100000 - BEFFFFFF
Memory Location: FEC00000 - FFFFFFFF

Hauptplatinenressourcen

Device Name: Hauptplatinenressourcen

[Assigned Resources]

Memory Location: FEC00000 - FEC00FFF
Memory Location: FEE00000 - FEE00FFF

[Alternative 1]

Memory Location: FEC00000 - FEC00FFF
Memory Location: FEE00000 - FEE00FFF

Hauptplatinenressourcen

Device Name: Hauptplatinenressourcen

[Assigned Resources]

I/O Port: 0010 - 001F
I/O Port: 0022 - 003F

I/O Port:	0044 - 005F
I/O Port:	0062 - 0063
I/O Port:	0065 - 006F
I/O Port:	0072 - 007F
I/O Port:	0080
I/O Port:	0084 - 0086
I/O Port:	0088
I/O Port:	008C - 008E
I/O Port:	0090 - 009F
I/O Port:	00A2 - 00BF
I/O Port:	00E0 - 00EF
I/O Port:	04D0 - 04D1
I/O Port:	0800 - 080F
I/O Port:	2000 - 207F
I/O Port:	2080 - 20FF
I/O Port:	2400 - 247F
I/O Port:	2480 - 24FF
I/O Port:	2800 - 287F
I/O Port:	2880 - 28FF
I/O Port:	2C00 - 2C7F
I/O Port:	2C80 - 2CFF
Memory Location:	000D0000 - 000D3FFF
Memory Location:	000D4000 - 000D7FFF
Memory Location:	000DE000 - 000DFFFF
Memory Location:	FEA80000 - FEABFFFF
Memory Location:	FEE01000 - FEEFFFFF

[Alternative 1]

I/O Port:	0010 - 001F
I/O Port:	0022 - 003F
I/O Port:	0044 - 005F
I/O Port:	0062 - 0063
I/O Port:	0065 - 006F
I/O Port:	0072 - 007F
I/O Port:	0080
I/O Port:	0084 - 0086
I/O Port:	0088
I/O Port:	008C - 008E
I/O Port:	0090 - 009F
I/O Port:	00A2 - 00BF
I/O Port:	00E0 - 00EF
I/O Port:	04D0 - 04D1
I/O Port:	0800 - 080F
I/O Port:	2000 - 207F
I/O Port:	2080 - 20FF
I/O Port:	2400 - 247F
I/O Port:	2480 - 24FF
I/O Port:	2800 - 287F
I/O Port:	2880 - 28FF
I/O Port:	2C00 - 2C7F
I/O Port:	2C80 - 2CFF
Memory Location:	000D0000 - 000D3FFF
Memory Location:	000D4000 - 000D7FFF
Memory Location:	000DE000 - 000DFFFF
Memory Location:	FEA80000 - FEABFFFF
Memory Location:	FEE01000 - FEEFFFFF

Hauptplatinenressourcen

Device Name: Hauptplatinenressourcen

[Assigned Resources]

Memory Location: E0000000 - EFFFFFFF

[Alternative 1]

Memory Location: E0000000 - EFFFFFFF

Hauptplatinenressourcen

Device Name: Hauptplatinenressourcen

[Assigned Resources]

Memory Location: D0000000 - DFFFFFFF

[Alternative 1]

Memory Location: D0000000 - DFFFFFFF

Hauptplatinenressourcen

Device Name: Hauptplatinenressourcen

[Assigned Resources]

I/O Port: 0A00 - 0A0F
I/O Port: 0A10 - 0A1F

[Alternative 1]

I/O Port: 0A00 - 0A0F
I/O Port: 0A10 - 0A1F

Numerischer Coprozessor

Device Name: Numerischer Coprozessor

[Assigned Resources]

I/O Port: 00F0 - 00FF
IRQ: 13

[Alternative 1]

I/O Port: 00F0 - 00FF
IRQ: 13

ACPI-Einschaltknopf

Device Name:	ACPI-Einschaltknopf

Microsoft PS/2-Maus

Device Name:	Microsoft PS/2-Maus

[Assigned Resources]

IRQ:	12

[Alternative 1]

IRQ:	12

SMBIOS DMI

BIOS

BIOS Vendor:	MS-7250
BIOS Version:	V3.10
BIOS Release Date:	04/07/2008
BIOS Start Segment:	F000
BIOS Size:	512 KBytes
ISA Support:	Present
MCA Support:	Not Present
EISA Support:	Not Present
PCI Support:	Present
PC Card (PCMCIA) Support:	Not Present
Plug-and-Play Support:	Present
APM Support:	Present
Flash BIOS:	Present
BIOS Shadow:	Present
VL-VESA Support:	Not Present
ESCD Support:	Present
Boot from CD:	Present
Selectable Boot:	Present
BIOS ROM Socketed:	Present
Boot from PC Card:	Not Present
EDD Support:	Present
NEC PC-98 Support:	Not Present
ACPI Support:	Present
USB Legacy Support:	Present
AGP Support:	Not Present

I2O Boot Support:	Not Present
LS-120 Boot Support:	Present
ATAPI ZIP Drive Boot Support:	Present
IEE1394 Boot Support:	Not Present
Smart Battery Support:	Not Present

System

System Manufacturer:	MSI
Product Name:	MS-7250
Product Version:	2.0
Product Serial Number:	To Be Filled By O.E.M.
SKU Number:	To Be Filled By O.E.M.
Family:	To Be Filled By O.E.M.

Mainboard

Mainboard Manufacturer:	MSI
Mainboard Name:	MS-7250
Mainboard Version:	2.0
Mainboard Serial Number:	To be filled by O.E.M.
Asset Tag:	To Be Filled By O.E.M.
Location in chassis:	To Be Filled By O.E.M.

System Enclosure

Manufacturer:	To Be Filled By O.E.M.
Case Type:	Desktop
Version:	To Be Filled By O.E.M.
Serial Number:	To Be Filled By O.E.M.
Asset Tag Number:	To Be Filled By O.E.M.

Processor

Processor Manufacturer:	AMD
Processor Version:	AMD Athlon(tm) 64 X2 Dual Core Processor 6000+
External Clock:	200 MHz
Maximum Clock Supported:	3000 MHz
Current Clock:	3015 MHz
CPU Socket:	Populated
CPU Status:	Enabled
Processor Type:	Central Processor
Processor Voltage:	1.5 V
Processor Upgrade:	Unknown
Socket Designation:	CPU 1

L1-Cache

Socket Designation:	L1-Cache
Cache State:	Enabled
Cache Type:	Internal, Data
Cache Scheme:	Write-Through and Write-Back
Supported SRAM Type:	Pipeline Burst
Current SRAM Type:	Pipeline Burst
Cache Speed:	Unknown
Error Correction Type:	
Maximum Cache Size:	256 KBytes
Installed Cache Size:	256 KBytes
Cache Associativity:	4-way Set-Associative

L2-Cache

Socket Designation:	L2-Cache
Cache State:	Enabled
Cache Type:	Internal, Unified
Cache Scheme:	Write-Through and Write-Back
Supported SRAM Type:	Pipeline Burst
Current SRAM Type:	Pipeline Burst
Cache Speed:	Unknown
Error Correction Type:	
Maximum Cache Size:	2048 KBytes
Installed Cache Size:	2048 KBytes
Cache Associativity:	4-way Set-Associative

L3-Cache

Socket Designation:	L3-Cache
Cache State:	Disabled
Cache Type:	Internal
Cache Scheme:	Unknown
Supported SRAM Type:	
Current SRAM Type:	
Cache Speed:	Unknown
Error Correction Type:	Unknown
Maximum Cache Size:	0 KBytes
Installed Cache Size:	0 KBytes
Cache Associativity:	Unknown

Memory Devices

Memory Controller

Error Detecting Method:	64-bit ECC
Error Correction:	None
Supported Interleave:	1-Way
Current Interleave:	1-Way
Max. Memory Module Size:	1024 MBytes
Supported Memory Speed:	70 ns, 60 ns
Supported Memory Type:	SIMM, DIMM, SDRAM
Supported Memory Voltage:	3.3 V
Associated Memory Slots:	4

DIMM0

Socket Designation:	DIMM0
Memory Type:	ECC, DIMM
Memory Speed:	160 ns
Installed size:	1024 MBytes
Enabled size:	1024 MBytes

DIMM1

Socket Designation:	DIMM1
Memory Type:	ECC, DIMM
Memory Speed:	162 ns
Installed size:	1024 MBytes
Enabled size:	1024 MBytes

DIMM2

Socket Designation:	DIMM2
Memory Type:	ECC, DIMM
Memory Speed:	164 ns
Installed size:	1024 MBytes
Enabled size:	1024 MBytes

DIMM3

Socket Designation:	DIMM3
Memory Type:	ECC, DIMM
Memory Speed:	166 ns
Installed size:	1024 MBytes
Enabled size:	1024 MBytes

Port Connectors

Mouse Port

Port Type:	Mouse Port
Internal Reference:	J1A1
Internal Connector Type:	None
External Reference:	PS2Mouse
External Connector Type:	PS/2

Keyboard Port

Port Type:	Keyboard Port
Internal Reference:	J1A1
Internal Connector Type:	None
External Reference:	Keyboard
External Connector Type:	PS/2

USB

Port Type:	USB
Internal Reference:	J2A2
Internal Connector Type:	None
External Reference:	USB1
External Connector Type:	Access Bus (USB)

USB

Port Type:	USB
Internal Reference:	J2A2
Internal Connector Type:	None
External Reference:	USB2
External Connector Type:	Access Bus (USB)

Parallel Port ECP/EPP

Port Type:	Parallel Port ECP/EPP
Internal Reference:	J4A1

Internal Connector Type:	None
External Reference:	LPT 1
External Connector Type:	DB25 pin male

Serial Port 16550A Compatible

Port Type:	Serial Port 16550A Compatible
Internal Reference:	J2A1
Internal Connector Type:	None
External Reference:	COM A
External Connector Type:	DB-9 pin male

Audio Port

Port Type:	Audio Port
Internal Reference:	J6A1
Internal Connector Type:	None
External Reference:	Audio Mic In
External Connector Type:	Mini-jack (headphones)

Audio Port

Port Type:	Audio Port
Internal Reference:	J6A1
Internal Connector Type:	None
External Reference:	Audio Line In
External Connector Type:	Mini-jack (headphones)

Audio Port

Port Type:	Audio Port
Internal Reference:	J6B1 - AUX IN
Internal Connector Type:	On Board Sound Input from CD-ROM
External Reference:	Unknown
External Connector Type:	None

Audio Port

Port Type:	Audio Port
Internal Reference:	J6B2 - CDIN
Internal Connector Type:	On Board Sound Input from CD-ROM
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J6J2 - PRI IDE
Internal Connector Type:	On Board IDE
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J6J1 - SEC IDE
Internal Connector Type:	On Board IDE
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J4J1 - FLOPPY
Internal Connector Type:	On Board Floppy
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J9H1 - FRONT PNL
Internal Connector Type:	9 Pin Dual Inline (pin 10 cut)
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J1B1 - CHASSIS REAR FAN
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J2F1 - CPU FAN
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J8B4 - FRONT FAN
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J9G2 - FNT USB
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J6C3 - FP AUD
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J9G1 - CONFIG
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown

Internal Reference:	J8C1 - SCSI LED
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J9J2 - INTRUDER
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J9G4 - ITP
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

Port Connector

Port Type:	Unknown
Internal Reference:	J2H1 - MAIN POWER
Internal Connector Type:	Unknown
External Reference:	Unknown
External Connector Type:	None

System Slots

AGP

Slot Designation:	AGP
Slot Type:	AGP 4X
Slot Usage:	In use
Slot Data Bus Width:	32-bit
Slot Length:	Short

PCI1

Slot Designation:	PCI1
Slot Type:	PCI
Slot Usage:	In use
Slot Data Bus Width:	32-bit
Slot Length:	Short

On Board Device

Device Description:	To Be Filled By O.E.M.
Device Type:	Video Adapter
Device Status:	<Enabled>

OEM Strings

To Be Filled By O.E.M.

System Configuration Options

To Be Filled By O.E.M.

BIOS Language

en|US|iso8859-1 <Active>

Physical Memory Array

Array Location:	System board
Array Use:	System memory
Error Detecting Method:	None
Memory Capacity:	8388608 KBytes
Memory Devices:	4

Memory Array Mapped Address

Starting Address:	00000000
Ending Address:	00503FFF
Partition Width:	1

Memory Device

Total Width:	64 bits
Data Width:	72 bits
Device Size:	1024 MBytes
Device Form Factor:	DIMM
Device Locator:	DIMM0
Bank Locator:	BANK0
Device Type:	DDR2 SDRAM
Device Type Detail:	Synchronous
Manufacturer:	Manufacturer0
Serial Number:	SerNum0
Part Number:	PartNum0
Asset Tag:	AssetTagNum0

Memory Device Mapped Address

Starting Address:	00000000
Ending Address:	000FFFFF
Partition Row Position:	1
Interleave Position:	Non-interleaved
Interleave Data Depth:	0

Memory Device

Total Width:	64 bits
Data Width:	72 bits
Device Size:	1024 MBytes
Device Form Factor:	DIMM
Device Locator:	DIMM1
Bank Locator:	BANK1
Device Type:	DDR2 SDRAM
Device Type Detail:	Synchronous
Manufacturer:	Manufacturer1
Serial Number:	SerNum1
Part Number:	PartNum1
Asset Tag:	AssetTagNum1

Memory Device Mapped Address

Starting Address:	00100000
Ending Address:	001FFFFF
Partition Row Position:	1
Interleave Position:	Non-interleaved
Interleave Data Depth:	0

Memory Device

Total Width:	64 bits
Data Width:	72 bits
Device Size:	1024 MBytes
Device Form Factor:	DIMM
Device Locator:	DIMM2
Bank Locator:	BANK2
Device Type:	DDR2 SDRAM
Device Type Detail:	Synchronous
Manufacturer:	Manufacturer2
Serial Number:	SerNum2
Part Number:	PartNum2
Asset Tag:	AssetTagNum2

Memory Device Mapped Address

Starting Address:	00200000
Ending Address:	002FFFFF
Partition Row Position:	1
Interleave Position:	Non-interleaved
Interleave Data Depth:	0

Memory Device

Total Width:	64 bits
Data Width:	72 bits
Device Size:	1024 MBytes
Device Form Factor:	DIMM
Device Locator:	DIMM3
Bank Locator:	BANK3
Device Type:	DDR2 SDRAM
Device Type Detail:	Synchronous
Manufacturer:	Manufacturer3
Serial Number:	SerNum3
Part Number:	PartNum3
Asset Tag:	AssetTagNum3

Memory Device Mapped Address

Starting Address:	00300000
Ending Address:	003FFFFF
Partition Row Position:	1
Interleave Position:	Non-interleaved
Interleave Data Depth:	0

Memory

[General information]

Total Memory Size:	4 GBytes
Total Memory Size [MB]:	4096

[Current Performance Settings]

Current Memory Clock:	375.0 MHz
Current Timing (tCAS-tRCD-tRP-tRAS):	5.0-6-5-18
Memory Runs At:	Dual-Channel
Command Rate:	2T
Write to Read Delay (tWR_RD) Same Rank (tWTR):	3T
Write to Precharge Delay (tWTP/tWR):	6T
Row Cycle Time (tRC):	24T
Four Activate Window (tFAW):	14T

Row: 0 - 1024 MB PC2-6400 DDR2-SDRAM MDT Technologies (MCI Computer) MDT 1GB DDR2-80

[General Module Information]

Module Number:	0
Module Size:	**1024 MBytes**
Memory Type:	**DDR2-SDRAM**
DIMM Type:	Regular Unbuffered (UDIMM)
Error Check/Correction:	None
Memory Speed:	**400.0 MHz (PC2-6400)**
Module Manufacturer:	**MDT Technologies (MCI Computer)**
Module Model:	**MDT 1GB DDR2-80**
Serial Number:	169888494
Manufacturing Date:	Year: 2007, Week: 48

[Module characteristics]

Module Width:	64-bits
Module Voltage:	SSTL 1.8V
SPD Revision:	1.2
Number Of Ranks:	2
Row Address Bits:	14
Column Address Bits:	10
Number Of Banks:	4

[Module timing]

Supported Burst Lengths:	4, 8
Refresh Rate:	Reduced 0.5x (7.8 us)
Supported CAS Latencies (tCAS):	6.0, 5.0, 4.0
Min. RAS-to-CAS Delay (tRCD):	12.50 ns
Min. Row Precharge Time (tRP):	12.50 ns
Min. RAS Pulse Width (tRAS):	45 ns
Supported Module Timing at 400.0 MHz:	6.0-5-5-18
Supported Module Timing at 400.0 MHz:	5.0-5-5-18
Supported Module Timing at 266.7 MHz:	4.0-4-4-12

Min. Row-Activate To Row-Activate Delay (tRRD):	7.50 ns
Write Recovery Time (tWR):	15.00 ns
Internal write to read command delay (tWTR):	7.50 ns
Internal read to precharge command delay (tRTP):	7.50 ns
Minimum Activate to Activate/Refresh Time (tRC):	57.00 ns
Minimum Refresh to Activate/Refresh Command Period (tRFC):	75.00 ns
Address and Command Setup Time Before Clock (tIS):	0.15 ns
Address and Command Setup Time After Clock (tIH):	0.22 ns
Data Input Setup Time Before Strobe (tDS):	0.05 ns
Data Input Setup Time After Strobe (tDH):	0.12 ns

Row: 1 - 1024 MB PC2-6400 DDR2-SDRAM MDT Technologies (MCI Computer) MDT 1GB DDR2-80

[General Module Information]

Module Number:	1
Module Size:	**1024 MBytes**
Memory Type:	**DDR2-SDRAM**
DIMM Type:	Regular Unbuffered (UDIMM)
Error Check/Correction:	None
Memory Speed:	**400.0 MHz (PC2-6400)**
Module Manufacturer:	**MDT Technologies (MCI Computer)**
Module Model:	**MDT 1GB DDR2-80**
Serial Number:	169888494
Manufacturing Date:	Year: 2007, Week: 48

[Module characteristics]

Module Width:	64-bits
Module Voltage:	SSTL 1.8V
SPD Revision:	1.2
Number Of Ranks:	2
Row Address Bits:	14
Column Address Bits:	10
Number Of Banks:	4

[Module timing]

Supported Burst Lengths:	4, 8
Refresh Rate:	Reduced 0.5x (7.8 us)
Supported CAS Latencies (tCAS):	6.0, 5.0, 4.0
Min. RAS-to-CAS Delay (tRCD):	12.50 ns
Min. Row Precharge Time (tRP):	12.50 ns
Min. RAS Pulse Width (tRAS):	45 ns
Supported Module Timing at 400.0 MHz:	6.0-5-5-18
Supported Module Timing at 400.0 MHz:	5.0-5-5-18
Supported Module Timing at 266.7 MHz:	4.0-4-4-12

Min. Row-Activate To Row-Activate Delay (tRRD):	7.50 ns
Write Recovery Time (tWR):	15.00 ns
Internal write to read command delay (tWTR):	7.50 ns
Internal read to precharge command delay (tRTP):	7.50 ns
Minimum Activate to Activate/Refresh Time (tRC):	57.00 ns
Minimum Refresh to Activate/Refresh Command Period (tRFC):	75.00 ns
Address and Command Setup Time Before Clock (tIS):	0.15 ns
Address and Command Setup Time After Clock (tIH):	0.22 ns
Data Input Setup Time Before Strobe (tDS):	0.05 ns
Data Input Setup Time After Strobe (tDH):	0.12 ns

Row: 2 - 1024 MB PC2-6400 DDR2-SDRAM MDT Technologies (MCI Computer) MDT 1GB DDR2-80

[General Module Information]

Module Number:	2
Module Size:	**1024 MBytes**
Memory Type:	**DDR2-SDRAM**
DIMM Type:	Regular Unbuffered (UDIMM)
Error Check/Correction:	None
Memory Speed:	**400.0 MHz (PC2-6400)**
Module Manufacturer:	**MDT Technologies (MCI Computer)**
Module Model:	**MDT 1GB DDR2-80**
Serial Number:	169888494
Manufacturing Date:	Year: 2007, Week: 48

[Module characteristics]

Module Width:	64-bits
Module Voltage:	SSTL 1.8V
SPD Revision:	1.2
Number Of Ranks:	2
Row Address Bits:	14
Column Address Bits:	10
Number Of Banks:	4

[Module timing]

Supported Burst Lengths:	4, 8
Refresh Rate:	Reduced 0.5x (7.8 us)
Supported CAS Latencies (tCAS):	6.0, 5.0, 4.0
Min. RAS-to-CAS Delay (tRCD):	12.50 ns
Min. Row Precharge Time (tRP):	12.50 ns
Min. RAS Pulse Width (tRAS):	45 ns
Supported Module Timing at 400.0 MHz:	6.0-5-5-18
Supported Module Timing at 400.0 MHz:	5.0-5-5-18
Supported Module Timing at 266.7 MHz:	4.0-4-4-12
Min. Row-Activate To Row-Activate Delay (tRRD):	7.50 ns

Write Recovery Time (tWR):	15.00 ns
Internal write to read command delay (tWTR):	7.50 ns
Internal read to precharge command delay (tRTP):	7.50 ns
Minimum Activate to Activate/Refresh Time (tRC):	57.00 ns
Minimum Refresh to Activate/Refresh Command Period (tRFC):	75.00 ns
Address and Command Setup Time Before Clock (tIS):	0.15 ns
Address and Command Setup Time After Clock (tIH):	0.22 ns
Data Input Setup Time Before Strobe (tDS):	0.05 ns
Data Input Setup Time After Strobe (tDH):	0.12 ns

Row: 3 - 1024 MB PC2-6400 DDR2-SDRAM MDT Technologies (MCI Computer) MDT 1GB DDR2-80

[General Module Information]

Module Number:	3
Module Size:	**1024 MBytes**
Memory Type:	**DDR2-SDRAM**
DIMM Type:	Regular Unbuffered (UDIMM)
Error Check/Correction:	None
Memory Speed:	**400.0 MHz (PC2-6400)**
Module Manufacturer:	**MDT Technologies (MCI Computer)**
Module Model:	**MDT 1GB DDR2-80**
Serial Number:	169888494
Manufacturing Date:	Year: 2007, Week: 48

[Module characteristics]

Module Width:	64-bits
Module Voltage:	SSTL 1.8V
SPD Revision:	1.2
Number Of Ranks:	2
Row Address Bits:	14
Column Address Bits:	10
Number Of Banks:	4

[Module timing]

Supported Burst Lengths:	4, 8
Refresh Rate:	Reduced 0.5x (7.8 us)
Supported CAS Latencies (tCAS):	6.0, 5.0, 4.0
Min. RAS-to-CAS Delay (tRCD):	12.50 ns
Min. Row Precharge Time (tRP):	12.50 ns
Min. RAS Pulse Width (tRAS):	45 ns
Supported Module Timing at 400.0 MHz:	6.0-5-5-18
Supported Module Timing at 400.0 MHz:	5.0-5-5-18
Supported Module Timing at 266.7 MHz:	4.0-4-4-12
Min. Row-Activate To Row-Activate Delay (tRRD):	7.50 ns
Write Recovery Time (tWR):	15.00 ns

Internal write to read command delay (tWTR):	7.50 ns
Internal read to precharge command delay (tRTP):	7.50 ns
Minimum Activate to Activate/Refresh Time (tRC):	57.00 ns
Minimum Refresh to Activate/Refresh Command Period (tRFC):	75.00 ns
Address and Command Setup Time Before Clock (tIS):	0.15 ns
Address and Command Setup Time After Clock (tIH):	0.22 ns
Data Input Setup Time Before Strobe (tDS):	0.05 ns
Data Input Setup Time After Strobe (tDH):	0.12 ns

Bus

PCI Bus #0

nVIDIA MCP55 - Host Bridge (Upstream HyperTransport Controller)

[General Information]

Original Device Name:	**nVIDIA MCP55 - Host Bridge (Upstream HyperTransport Controller)**
Device Class:	**RAM/Memory Controller**
Revision ID:	A1
Bus Number:	0
Device Number:	0
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55P(XE) - LPC Bridge

[General Information]

Original Device Name:	**nVIDIA MCP55P(XE) - LPC Bridge**
Device Class:	**PCI-to-ISA Bridge**
Revision ID:	A2

Bus Number:	0
Device Number:	1
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A
I/O Base Address 0	2F00

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - SMBus Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - SMBus Controller**
Device Class:	**SMBus (System Management Bus)**
Revision ID:	A2
Bus Number:	0
Device Number:	1
Function Number:	1
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ11
Interrupt Pin:	INTA#
I/O Base Address 0	2900
I/O Base Address 4	2D00
I/O Base Address 5	2E00

[Features]

Bus Mastering:	Disabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - USB 1.1 OHCI Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - USB 1.1 OHCI Controller**
Device Class:	**Universal Serial Bus (USB)**
Revision ID:	A1
Bus Number:	0
Device Number:	2
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ22
Interrupt Pin:	INTA#
Memory Base Address 0	FEAFB000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - USB 2.0 EHCI Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - USB 2.0 EHCI Controller**
Device Class:	**Universal Serial Bus (USB)**
Revision ID:	A2
Bus Number:	0
Device Number:	2
Function Number:	1
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ23
Interrupt Pin:	INTB#
Memory Base Address 0	FEAFAC00

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - SATA Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - SATA Controller**
Device Class:	**IDE Controller**
Revision ID:	A2
Bus Number:	0
Device Number:	5
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ22
Interrupt Pin:	INTA#
I/O Base Address 0	D480
I/O Base Address 1	D400
I/O Base Address 2	D080
I/O Base Address 3	D000
I/O Base Address 4	CC00
Memory Base Address 5	FEAF9000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - SATA Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - SATA Controller**
Device Class:	**IDE Controller**
Revision ID:	A2
Bus Number:	0
Device Number:	5
Function Number:	1
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ23
Interrupt Pin:	INTB#
I/O Base Address 0	C880
I/O Base Address 1	C800
I/O Base Address 2	C480
I/O Base Address 3	C400
I/O Base Address 4	C080
Memory Base Address 5	FEAF8000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - SATA Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - SATA Controller**
Device Class:	**IDE Controller**
Revision ID:	A2
Bus Number:	0
Device Number:	5
Function Number:	2
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ21
Interrupt Pin:	INTC#
I/O Base Address 0	C000
I/O Base Address 1	BC00
I/O Base Address 2	B880
I/O Base Address 3	B800
I/O Base Address 4	B480
Memory Base Address 5	FEAF7000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - PCI-to-PCI Bridge

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI-to-PCI Bridge**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	6
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

PCI Bus #1

BrookTree BtV 360 MediaStream Controller

[General Information]

Original Device Name:	**BrookTree BtV 360 MediaStream Controller**
Device Class:	**Multimedia Video Adapter**
Revision ID:	11
Bus Number:	1
Device Number:	0
Function Number:	0
PCI Latency Timer:	64

[System Resources]

Interrupt Line:	IRQ19
Interrupt Pin:	INTA#
Memory Base Address 0	BFFFF000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Capable

BrookTree BtV 878 Video Capture Device (Audio Section)

[General Information]

Original Device Name:	**BrookTree BtV 878 Video Capture Device (Audio Section)**
Device Class:	**Unknown Multimedia Adapter**
Revision ID:	11
Bus Number:	1
Device Number:	0
Function Number:	1
PCI Latency Timer:	64

[System Resources]

Interrupt Line:	IRQ5
Interrupt Pin:	INTA#
Memory Base Address 0	BFFFE000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - High Definition Audio Controller

[General Information]

Original Device Name:	**nVIDIA MCP55 - High Definition Audio Controller**
Device Class:	**Mixed mode device**
Revision ID:	A2
Bus Number:	0
Device Number:	6
Function Number:	1
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ23
Interrupt Pin:	INTB#
Memory Base Address 0	FEAF0000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - Network Bus Enumerator

[General Information]

Original Device Name:	**nVIDIA MCP55 - Network Bus Enumerator**
Device Class:	**Unknown Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	8
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ20
Interrupt Pin:	INTA#

Memory Base Address 0	FEAF6000
I/O Base Address 1	0
Memory Base Address 2	FEAFA800
Memory Base Address 3	FEAFA400

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - Network Bus Enumerator

[General Information]

Original Device Name:	**nVIDIA MCP55 - Network Bus Enumerator**
Device Class:	**Unknown Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	9
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	IRQ20
Interrupt Pin:	INTA#
Memory Base Address 0	FEAF5000
I/O Base Address 1	0
Memory Base Address 2	FEAFA000
Memory Base Address 3	FEAF4C00

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Capable
Fast Back-to-Back Transactions:	Capable

nVIDIA MCP55 - PCI Express Root Port (X8 + VC1)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X8 + VC1)**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	10
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	8x
Current Link Width:	8x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex

Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

PCI Express x8 Bus #2

nVIDIA MCP55 - PCI Express Root Port (X4)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X4)**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	11
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	1x
Current Link Width:	4x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex
Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable

Fast Back-to-Back Transactions:	Not Capable

PCI Express x1 Bus #3

nVIDIA MCP55 - PCI Express Root Port (X4)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X4)**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	12
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	1x
Current Link Width:	4x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex
Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

PCI Express x1 Bus #4

nVIDIA MCP55 - PCI Express Root Port (X4 + VC1)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X4 + VC1)**

Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	13
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	1x
Current Link Width:	4x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex
Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

PCI Express x1 Bus #5

---

nVIDIA MCP55 - PCI Express Root Port (X8)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X8)**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	14
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	1x
Current Link Width:	8x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex

Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

PCI Express x1 Bus #6

---

nVIDIA MCP55 - PCI Express Root Port (X16)

[General Information]

Original Device Name:	**nVIDIA MCP55 - PCI Express Root Port (X16)**
Device Class:	**PCI-to-PCI Bridge**
Revision ID:	A2
Bus Number:	0
Device Number:	15
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	1.0
Maximum Link Width:	16x
Current Link Width:	16x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Root Port of PCI Express Root Complex
Slot Implemented:	Yes
Hot-Plug:	Not Capable
Hot-Plug Surprise:	Not Capable
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable

Fast Back-to-Back Transactions:	Not Capable

PCI Express x16 Bus #7

ATI RADEON HD 3850 256MB GDDR3

[General Information]

Original Device Name:	**ATI RADEON HD 3850/4730/4750 (RV670 PRO)**
Device Class:	**VGA Compatible Adapter**
Revision ID:	0
Bus Number:	7
Device Number:	0
Function Number:	0
PCI Latency Timer:	0

[PCI Express]

Version:	2.0
Maximum Link Width:	16x
Current Link Width:	16x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Legacy PCI Express Endpoint
Slot Implemented:	No
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	INTA#
Memory Base Address 0	C0000000
Memory Base Address 2	FEBF0000
I/O Base Address 4	E000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

ATI RV660/RV670 - High Definition Audio Controller

[General Information]

Original Device Name:	**ATI RV660/RV670 - High Definition Audio Controller**
Device Class:	**Mixed mode device**
Revision ID:	0
Bus Number:	7
Device Number:	0
Function Number:	1

PCI Latency Timer:	0

[PCI Express]

Version:	2.0
Maximum Link Width:	16x
Current Link Width:	16x
Maximum Link Speed:	2.5 Gb/s
Current Link Speed:	2.5 Gb/s
Device/Port Type:	Legacy PCI Express Endpoint
Slot Implemented:	No
Active State Power Management (ASPM) Support:	L0s and L1
Active State Power Management (ASPM) Status:	Disabled

[System Resources]

Interrupt Line:	IRQ18
Interrupt Pin:	INTB#
Memory Base Address 0	FEBEC000

[Features]

Bus Mastering:	Enabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

AMD Hammer - HyperTransport Technology Configuration

[General Information]

Original Device Name:	**AMD Hammer - HyperTransport Technology Configuration**
Device Class:	**Host-to-PCI Bridge**
Revision ID:	0
Bus Number:	0
Device Number:	24
Function Number:	0
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Disabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

[Routing Node 0]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 1]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 2]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 3]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 4]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 5]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 6]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Routing Node 7]

Request Route:	This node
Response Route:	This node
Broadcast Route:	This node

[Node ID]

This Node ID:	0
Node Count:	1
HyperTransport I/O Hub Node ID:	0
Lock Controller Node ID:	0
CPU Count:	2

[Unit ID]

CPU0 Unit ID:	0
CPU1 Unit ID:	1
Memory Controller Unit ID:	2
Host Bridge Unit ID:	3
HyperTransport I/O Hub Link ID:	LDT0

[HyperTransport Transaction Control]

Medium priority isochronous writes:	Enabled
High priority isochronous writes:	Enabled
Low priority writes:	Enabled
High priority CPU reads:	Disabled
High-priority bypass count:	3
Medium-priority bypass count:	3
Downstream non-posted request limit:	1
Sequence ID source node:	Disabled
APIC extended spurious vector:	Writable
APIC extended ID:	8 bits
APIC extended broadcast ID:	FFh
Local interrupt conversion:	Disabled
Coherent HyperTransport configuration space range:	Limited
Buffer release priority select:	8 packets
Change ISOC to Ordered:	Isochronous prioritization
Response PassPW:	Enabled

Fill probe:	Enabled
Remote probe memory cancel:	Enabled
Probe memory cancel:	Enabled
CPU Read response PassPW:	Disabled
CPU request PassPW:	Disabled
CPU1:	Enabled
Memory controller target start:	Enabled
Write doubleword probes:	Enabled
Write byte probes:	Enabled
Read doubleword probe:	Enabled
Read byte probe:	Enabled

[HyperTransport Initialization Control]

Default Link:	CPU on same node
Request:	Enabled
Routing Table:	Enabled

[LDT0 Capability]

Drop on Uninitialized Link:	No
Inbound End-of-Chain Error:	No
Act As Slave:	No
Host Hide:	Yes
Chain Side:	0
Device Number:	0
Double Ended:	No

[LDT0 Link Control]

Doubleword Flow Control Out:	Disabled
Link Width Out:	16 bits
Doubleword Flow Control In:	Disabled
Link Width In:	16 bits
Doubleword Flow Control Out:	Not supported
Max. Link Width Out:	16 bits
Doubleword Flow Control In:	Not supported
Max. Link Width In:	16 bits
Extended Control Time During Initialization:	>=16 bit times
HyperTransport Stop Tristate:	Disabled
Isochronous:	Disabled
CRC Error On Incoming Link (Higher Byte):	Not detected
CRC Error On Incoming Link (Lower Byte):	Not detected
Transmitter:	On
Receiver:	On
Initialization Complete:	Yes
Link Failure:	Not detected
CRC Flood:	Disabled

[LDT0 Link Frequency Capability]

Link 200 MHz Frequency Capability:	Capable
Link 300 MHz Frequency Capability:	Not capable
Link 400 MHz Frequency Capability:	Capable
Link 500 MHz Frequency Capability:	Not capable
Link 600 MHz Frequency Capability:	Capable
Link 800 MHz Frequency Capability:	Capable
Link 1000 MHz Frequency Capability:	Capable
Link 1200 MHz Frequency Capability:	Not capable
Link 1400 MHz Frequency Capability:	Not capable
Link 1600 MHz Frequency Capability:	Not capable
Link 1800 MHz Frequency Capability:	Not capable
Link 2000 MHz Frequency Capability:	Not capable
Link 2200 MHz Frequency Capability:	Not capable
Link 2400 MHz Frequency Capability:	Not capable
Link 2600 MHz Frequency Capability:	Not capable

Link 100 MHz Frequency Capability:	Capable
Current Link Frequency:	1000 MHz
Link Major Revision:	1
Link Minor Revision:	2

[LDT0 Feature Capability]

Extended Register Set:	Not supported
Extended CTL:	Not required
CRC Test Mode:	Not supported
HyperTransport Stop Mode:	Supported
Isochronous Flow Control Mode:	Not supported

[LDT0 Buffer Count]

Response Data Buffer Count:	1
Posted Request Data Buffer Count:	6
Request Data Buffer Count:	1
Probe Buffer Count:	0
Response Buffer Count:	1
Posted Request Buffer Count:	6
Request Buffer Count:	9

[LDT0 Bus Number]

Subordinate Bus Number:	255
Secondary Bus Number:	0
Primary Bus Number:	0

[LDT0 Type]

Link Connect Pending:	No
UniP-cLDT:	Normal coherent/Noncoherent
Non Coherent:	Noncoherent technology
Initialization Complete:	Yes
Link Connect Status:	Connected

[LDT1 Capability]

Drop on Uninitialized Link:	No
Inbound End-of-Chain Error:	No
Act As Slave:	No
Host Hide:	No
Chain Side:	0
Device Number:	0
Double Ended:	No

[LDT1 Link Control]

Doubleword Flow Control Out:	Disabled
Link Width Out:	8 bits
Doubleword Flow Control In:	Disabled
Link Width In:	8 bits
Doubleword Flow Control Out:	Not supported
Max. Link Width Out:	8 bits
Doubleword Flow Control In:	Not supported
Max. Link Width In:	8 bits
Extended Control Time During Initialization:	>=16 bit times
HyperTransport Stop Tristate:	Disabled
Isochronous:	Disabled
CRC Error On Incoming Link (Higher Byte):	Not detected
CRC Error On Incoming Link (Lower Byte):	Not detected
Transmitter:	On
Receiver:	On
Initialization Complete:	No
Link Failure:	Not detected
CRC Flood:	Disabled

[LDT1 Link Frequency Capability]

Link 200 MHz Frequency Capability:	Not capable
Link 300 MHz Frequency Capability:	Not capable
Link 400 MHz Frequency Capability:	Not capable
Link 500 MHz Frequency Capability:	Not capable
Link 600 MHz Frequency Capability:	Not capable
Link 800 MHz Frequency Capability:	Not capable
Link 1000 MHz Frequency Capability:	Not capable
Link 1200 MHz Frequency Capability:	Not capable
Link 1400 MHz Frequency Capability:	Not capable
Link 1600 MHz Frequency Capability:	Not capable
Link 1800 MHz Frequency Capability:	Not capable
Link 2000 MHz Frequency Capability:	Not capable
Link 2200 MHz Frequency Capability:	Not capable
Link 2400 MHz Frequency Capability:	Not capable
Link 2600 MHz Frequency Capability:	Not capable
Link 100 MHz Frequency Capability:	Not capable
Current Link Frequency:	200 MHz
Link Major Revision:	0
Link Minor Revision:	0

[LDT1 Feature Capability]

Extended Register Set:	Not supported
Extended CTL:	Not required
CRC Test Mode:	Not supported
HyperTransport Stop Mode:	Not supported
Isochronous Flow Control Mode:	Not supported

[LDT1 Buffer Count]

Response Data Buffer Count:	0
Posted Request Data Buffer Count:	0
Request Data Buffer Count:	0
Probe Buffer Count:	0
Response Buffer Count:	0
Posted Request Buffer Count:	0
Request Buffer Count:	0

[LDT1 Bus Number]

Subordinate Bus Number:	0
Secondary Bus Number:	0
Primary Bus Number:	0

[LDT1 Type]

Link Connect Pending:	No
UniP-cLDT:	Normal coherent/Noncoherent
Non Coherent:	Coherent technology
Initialization Complete:	No
Link Connect Status:	Not connected

[LDT2 Capability]

Drop on Uninitialized Link:	No
Inbound End-of-Chain Error:	No
Act As Slave:	No
Host Hide:	No
Chain Side:	0
Device Number:	0
Double Ended:	No

[LDT2 Link Control]

Doubleword Flow Control Out:	Disabled

Link Width Out:	8 bits
Doubleword Flow Control In:	Disabled
Link Width In:	8 bits
Doubleword Flow Control Out:	Not supported
Max. Link Width Out:	8 bits
Doubleword Flow Control In:	Not supported
Max. Link Width In:	8 bits
Extended Control Time During Initialization:	>=16 bit times
HyperTransport Stop Tristate:	Disabled
Isochronous:	Disabled
CRC Error On Incoming Link (Higher Byte):	Not detected
CRC Error On Incoming Link (Lower Byte):	Not detected
Transmitter:	On
Receiver:	On
Initialization Complete:	No
Link Failure:	Not detected
CRC Flood:	Disabled

[LDT2 Link Frequency Capability]

Link 200 MHz Frequency Capability:	Not capable
Link 300 MHz Frequency Capability:	Not capable
Link 400 MHz Frequency Capability:	Not capable
Link 500 MHz Frequency Capability:	Not capable
Link 600 MHz Frequency Capability:	Not capable
Link 800 MHz Frequency Capability:	Not capable
Link 1000 MHz Frequency Capability:	Not capable
Link 1200 MHz Frequency Capability:	Not capable
Link 1400 MHz Frequency Capability:	Not capable
Link 1600 MHz Frequency Capability:	Not capable
Link 1800 MHz Frequency Capability:	Not capable
Link 2000 MHz Frequency Capability:	Not capable
Link 2200 MHz Frequency Capability:	Not capable
Link 2400 MHz Frequency Capability:	Not capable
Link 2600 MHz Frequency Capability:	Not capable
Link 100 MHz Frequency Capability:	Not capable
Current Link Frequency:	200 MHz
Link Major Revision:	0
Link Minor Revision:	0

[LDT2 Feature Capability]

Extended Register Set:	Not supported
Extended CTL:	Not required
CRC Test Mode:	Not supported
HyperTransport Stop Mode:	Not supported
Isochronous Flow Control Mode:	Not supported

[LDT2 Buffer Count]

Response Data Buffer Count:	0
Posted Request Data Buffer Count:	0
Request Data Buffer Count:	0
Probe Buffer Count:	0
Response Buffer Count:	0
Posted Request Buffer Count:	0
Request Buffer Count:	0

[LDT2 Bus Number]

Subordinate Bus Number:	0
Secondary Bus Number:	0
Primary Bus Number:	0

[LDT2 Type]

Link Connect Pending:	No
UniP-cLDT:	Normal coherent/Noncoherent
Non Coherent:	Coherent technology
Initialization Complete:	No
Link Connect Status:	Not connected

AMD Hammer - Address Map

[General Information]

Original Device Name:	**AMD Hammer - Address Map**
Device Class:	**Host-to-PCI Bridge**
Revision ID:	0
Bus Number:	0
Device Number:	24
Function Number:	1
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Disabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

[DRAM Base 0]

DRAM Base Address:	000
Interleave:	None
Access:	Read/Write

[DRAM Limit 0]

DRAM Limit Address:	140000000
Interleave:	None
Destination Node ID:	0

[DRAM Base 1]

DRAM Base Address:	000
Interleave:	None
Access:	No access

[DRAM Limit 1]

DRAM Limit Address:	000
Interleave:	None
Destination Node ID:	1

[DRAM Base 2]

DRAM Base Address:	000
Interleave:	None
Access:	No access

[DRAM Limit 2]

DRAM Limit Address:	000
Interleave:	None
Destination Node ID:	2

[DRAM Base 3]

DRAM Base Address: 000
Interleave: None
Access: No access

[DRAM Limit 3]

DRAM Limit Address: 000
Interleave: None
Destination Node ID: 3

[DRAM Base 4]

DRAM Base Address: 000
Interleave: None
Access: No access

[DRAM Limit 4]

DRAM Limit Address: 000
Interleave: None
Destination Node ID: 4

[DRAM Base 5]

DRAM Base Address: 000
Interleave: None
Access: No access

[DRAM Limit 5]

DRAM Limit Address: 000
Interleave: None
Destination Node ID: 5

[DRAM Base 6]

DRAM Base Address: 000
Interleave: None
Access: No access

[DRAM Limit 6]

DRAM Limit Address: 000
Interleave: None
Destination Node ID: 6

[DRAM Base 7]

DRAM Base Address: 000
Interleave: None
Access: No access

[DRAM Limit 7]

DRAM Limit Address: 000
Interleave: None
Destination Node ID: 7

[Memory-Mapped I/O Base 0]

Memory-Mapped I/O Base Address: E0000000
Lock: Unlocked
CPU Access: Enabled
Access: Read/Write

[Memory-Mapped I/O Limit 0]

Memory-Mapped I/O Limit Address: EFFF0000
Non-Posted: Non-Posted
Destination Link ID: 0
Destination Node ID: 0

[Memory-Mapped I/O Base 1]

Memory-Mapped I/O Base Address:	000
Lock:	Unlocked
CPU Access:	Enabled
Access:	No access

[Memory-Mapped I/O Limit 1]

Memory-Mapped I/O Limit Address:	000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 2]

Memory-Mapped I/O Base Address:	000
Lock:	Unlocked
CPU Access:	Enabled
Access:	No access

[Memory-Mapped I/O Limit 2]

Memory-Mapped I/O Limit Address:	000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 3]

Memory-Mapped I/O Base Address:	000
Lock:	Unlocked
CPU Access:	Enabled
Access:	No access

[Memory-Mapped I/O Limit 3]

Memory-Mapped I/O Limit Address:	000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 4]

Memory-Mapped I/O Base Address:	000
Lock:	Unlocked
CPU Access:	Enabled
Access:	No access

[Memory-Mapped I/O Limit 4]

Memory-Mapped I/O Limit Address:	000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 5]

Memory-Mapped I/O Base Address:	000
Lock:	Unlocked
CPU Access:	Enabled
Access:	No access

[Memory-Mapped I/O Limit 5]

Memory-Mapped I/O Limit Address:	000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 6]

Memory-Mapped I/O Base Address:	A0000
Lock:	Unlocked
CPU Access:	Enabled
Access:	Read/Write

[Memory-Mapped I/O Limit 6]

Memory-Mapped I/O Limit Address:	B0000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[Memory-Mapped I/O Base 7]

Memory-Mapped I/O Base Address:	BF000000
Lock:	Unlocked
CPU Access:	Enabled
Access:	Read/Write

[Memory-Mapped I/O Limit 7]

Memory-Mapped I/O Limit Address:	FE0B0000
Non-Posted:	Posted
Destination Link ID:	0
Destination Node ID:	0

[PCI I/O Base 0]

PCI I/O Base Address:	1
ISA Enable:	Disabled
VGA Enable:	Enabled
Access:	Read/Write

[PCI I/O Limit 0]

PCI I/O Limit Address:	FFF
Destination Link ID:	0
Destination Node ID:	0

[PCI I/O Base 1]

PCI I/O Base Address:	0
ISA Enable:	Disabled
VGA Enable:	Disabled
Access:	No access

[PCI I/O Limit 1]

PCI I/O Limit Address:	0
Destination Link ID:	0
Destination Node ID:	0

[PCI I/O Base 2]

PCI I/O Base Address:	2
ISA Enable:	Disabled
VGA Enable:	Disabled
Access:	Read/Write

[PCI I/O Limit 2]

PCI I/O Limit Address:	2
Destination Link ID:	0
Destination Node ID:	0

[PCI I/O Base 3]

PCI I/O Base Address:	0

ISA Enable:	Disabled
VGA Enable:	Disabled
Access:	No access

[PCI I/O Limit 3]

PCI I/O Limit Address:	0
Destination Link ID:	0
Destination Node ID:	0

[Config Base and Limit 0]

Bus Number Limit:	FF
Bus Number Base:	0
Destination Link ID:	0
Destination Node ID:	0
Device Number Compare:	Disabled
Access:	Read/Write

[Config Base and Limit 1]

Bus Number Limit:	0
Bus Number Base:	0
Destination Link ID:	0
Destination Node ID:	0
Device Number Compare:	Disabled
Access:	No access

[Config Base and Limit 2]

Bus Number Limit:	0
Bus Number Base:	0
Destination Link ID:	0
Destination Node ID:	0
Device Number Compare:	Disabled
Access:	No access

[Config Base and Limit 3]

Bus Number Limit:	0
Bus Number Base:	0
Destination Link ID:	0
Destination Node ID:	0
Device Number Compare:	Disabled
Access:	No access

[DRAM Hole]

DRAM Hole Base Address:	BF000000
DRAM Hole Offset:	0
DRAM Hole Enable:	Enabled

AMD Hammer - DRAM Controller

[General Information]

Original Device Name:	**AMD Hammer - DRAM Controller**
Device Class:	**Host-to-PCI Bridge**
Revision ID:	0
Bus Number:	0
Device Number:	24
Function Number:	2
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Disabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

[DRAM CS Base Address 0]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Enabled

[DRAM CS Base Address 1]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Enabled

[DRAM CS Base Address 2]

Base Address:	2000
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Enabled

[DRAM CS Base Address 3]

Base Address:	2000
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Enabled

[DRAM CS Base Address 4]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Disabled

[DRAM CS Base Address 5]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Disabled

[DRAM CS Base Address 6]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Disabled

[DRAM CS Base Address 7]

Base Address:	00
Memory Test:	Passed
Spare Rank:	Not
Chip-Select Bank:	Disabled

[DRAM CS Mask 0]

Address Mask:	E03C000

[DRAM CS Mask 1]

Address Mask:	E03C000

[DRAM CS Mask 2]

Address Mask:	00

[DRAM CS Mask 3]

Address Mask:	00

[DRAM CS Mask 4 [Sub F]]

Address Mask:	00

[DRAM CS Mask 5 [Sub F]]

Address Mask:	00

[DRAM CS Mask 6 [Sub F]]

Address Mask:	00

[DRAM CS Mask 7 [Sub F]]

Address Mask:	00

AMD Hammer - Miscellaneous Control

[General Information]

Original Device Name:	**AMD Hammer - Miscellaneous Control**
Device Class:	**Host-to-PCI Bridge**
Revision ID:	0
Bus Number:	0
Device Number:	24
Function Number:	3
PCI Latency Timer:	0

[System Resources]

Interrupt Line:	N/A
Interrupt Pin:	N/A

[Features]

Bus Mastering:	Disabled
Running At 66 MHz:	Not Capable
Fast Back-to-Back Transactions:	Not Capable

[MCA NB Control]

DRAM Parity Error Reporting [F]:	Enabled
Watchdog Timer Error Reporting:	Enabled
Atomic Read-Modify-Write Error Reporting:	Enabled
GART Table Walk Error Reporting:	Disabled
Target Abort Error Reporting:	Enabled
Master Abort Error Reporting:	Enabled
HyperTransport Link 2 Sync Packet Error Reporting:	Enabled
HyperTransport Link 1 Sync Packet Error Reporting:	Enabled
HyperTransport Link 0 Sync Packet Error Reporting:	Enabled
HyperTransport Link 2 CRC Error Reporting:	Enabled
HyperTransport Link 1 CRC Error Reporting:	Enabled
HyperTransport Link 0 CRC Error Reporting:	Enabled
Uncorrectable ECC Error Reporting:	Enabled
Correctable ECC Error Reporting:	Enabled

[MCA NB Configuration]

Sync Flood on DRAM Address Parity Error [F]:	Disabled
Master Abort CPU Error Response [F]:	Enabled
Target Abort CPU Error Response [F]:	Enabled
Northbridge MCA to CPU 0:	Enabled
PCI Configuration CPU Error Response:	Disabled
I/O Read Data Error Log:	Disabled
Chip-Kill ECC Mode:	Disabled
ECC:	Disabled
Sync Flood On Any Error:	Disabled
Sync Flood on Watchdog Timer Error:	Enabled
HyperTransport Link Select for CRC Error Generation:	Link 0
Watchdog Timer Time Base:	1 ms
Watchdog Timer Count:	4095
Watchdog Timer:	Enabled
I/O Error Response:	Enabled
CPU Error Response:	Disabled
I/O Master Abort Error Response:	Enabled
Sync Packet Propagation:	Enabled
Sync Packet Generation:	Enabled
Sync Flood on Uncorrectable ECC Error:	Disabled
CPU Read Data Error Log:	Disabled
CPU ECC Error Log:	Disabled

[SRI-to-XBAR Buffer Count]

Downstream Posted Request Buffer Count:	1
Downstream Request Buffer Count:	1
Upstream Response Data Buffer Count:	1
Display Refresh Request Buffer Count:	0
Request Data Buffer Count:	0
Upstream Response Buffer Count:	1
Upstream Posted Request Buffer Count:	1
Upstream Request Buffer Count:	1

[XBAR-to-SRI Buffer Count]

Downstream Posted Request Buffer Count:	1
Downstream Request Buffer Count:	1
Display Refresh Request Buffer Count:	0
Probe Buffer Count:	3
Upstream Posted Request Buffer Count:	2
Upstream Request Buffer Count:	2

[MCT-to-XBAR Buffer Count]

Response Data Buffer Count:	8
Probe Buffer Count:	1
Response Buffer Count:	11

[Free List Buffer Count]

SRI to XBAR Free Response Data Buffer Count:	2
SRI to XBAR Free Response Buffer Count:	2
SRI to XBAR Free Request Buffer Count:	2
SRI Free Command Buffer Count:	6

[Power Management Control]

Power Management Mode 7 Clock Divisor:	32
Power Management Mode 7 Alternate VID Change:	Disabled
Power Management Mode 7 FID/VID Change:	Disabled
Power Management Mode 7 NB Low Power:	Disabled
Power Management Mode 7 CPU Low Power:	Enabled

Power Management Mode 6 Clock Divisor:	16
Power Management Mode 6 Alternate VID Change:	Disabled
Power Management Mode 6 FID/VID Change:	Disabled
Power Management Mode 6 NB Low Power:	Enabled
Power Management Mode 6 CPU Low Power:	Enabled
Power Management Mode 5 Clock Divisor:	32
Power Management Mode 5 Alternate VID Change:	Disabled
Power Management Mode 5 FID/VID Change:	Disabled
Power Management Mode 5 NB Low Power:	Disabled
Power Management Mode 5 CPU Low Power:	Enabled
Power Management Mode 4 Clock Divisor:	16
Power Management Mode 4 Alternate VID Change:	Disabled
Power Management Mode 4 FID/VID Change:	Disabled
Power Management Mode 4 NB Low Power:	Enabled
Power Management Mode 4 CPU Low Power:	Enabled
Power Management Mode 3 Clock Divisor:	32
Power Management Mode 3 Alternate VID Change:	Disabled
Power Management Mode 3 FID/VID Change:	Disabled
Power Management Mode 3 NB Low Power:	Enabled
Power Management Mode 3 CPU Low Power:	Enabled
Power Management Mode 2 Clock Divisor:	8
Power Management Mode 2 Alternate VID Change:	Disabled
Power Management Mode 2 FID/VID Change:	Enabled
Power Management Mode 2 NB Low Power:	Enabled
Power Management Mode 2 CPU Low Power:	Enabled
Power Management Mode 1 Clock Divisor:	8
Power Management Mode 1 Alternate VID Change:	Disabled
Power Management Mode 1 FID/VID Change:	Disabled
Power Management Mode 1 NB Low Power:	Disabled
Power Management Mode 1 CPU Low Power:	Disabled
Power Management Mode 0 Clock Divisor:	8
Power Management Mode 0 Alternate VID Change:	Disabled
Power Management Mode 0 FID/VID Change:	Disabled
Power Management Mode 0 NB Low Power:	Disabled
Power Management Mode 0 CPU Low Power:	Disabled

[GART Aperture Control]

GART Table Walk Probes:	Enabled
GART I/O Accesses:	Enabled
GART CPU Accesses:	Enabled
GART Size:	32 MBytes
GART:	Disabled

[GART Aperture Base]

GART Aperture Base Address:	2414000000

[GART Table Base]

GART Table Base Address:	8B50A08000

[Clock Power/Timing]

HyperTransport CLK PLL Lock Counter:	13
Link Reconnect Delay:	10 us
Clock Ramp Hysteresis:	2000 ns
Time to Drive Sleep VID:	0 system clocks
Good Phase Error:	400(B)/200(C) system clocks
Ramp VID Offset:	0 mV
Alternate VID:	6
Voltage Regulator Stabilization Time:	0 ns

[HyperTransportt FIFO Read Pointer Optimization]

Change Read Pointer For HyperTransport Link 2 Transmitter:	Move RdPtr closer to WrPtr by 2 HyperTransport clock periods

Change Read Pointer For HyperTransport Link 2 Receiver:	Move RdPtr closer to WrPtr by 6 HyperTransport clock periods
Change Read Pointer For HyperTransport Link 1 Transmitter:	Move RdPtr closer to WrPtr by 2 HyperTransport clock periods
Change Read Pointer For HyperTransport Link 1 Receiver:	Move RdPtr closer to WrPtr by 6 HyperTransport clock periods
Change Read Pointer For HyperTransport Link 0 Transmitter:	Move RdPtr closer to WrPtr by 2 HyperTransport clock periods
Change Read Pointer For HyperTransport Link 0 Receiver:	Move RdPtr closer to WrPtr by 5 HyperTransport clock periods

[Thermtrip Status]

Case Temperature Specification (TCaseMax) [Rev E]:	Not defined
Diode Offset Sign [Rev E]:	Positive
Tj Offset [Rev F]:	0 °C
Current Temperature [Rev F]:	32 °C
Diode Offset:	25 °C
Thermtrip Enabled:	Enabled
Thermtrip Sense (Core1):	Occurs
Thermtrip Sense (Core0):	Occurs
Thermtrip Status:	Occurs

[Northbridge Capabilities]

Multi-Core Capability:	Dual-Core
HTC (Hardware Thermal Control):	Capable
On-chip Memory Controller:	Capable
Maximum DRAM Frequency:	No limit
Chip-Kill ECC:	Capable
ECC:	Capable
Big MP:	Not capable
MP:	Not capable
128-Bit DRAM:	Capable

Video Adapter

ATI RADEON HD 3850/4730/4750 (RV670)

[Video chipset]

Video Chipset:	**ATI RADEON HD 3850/4730/4750 (RV670)**
Video Memory:	256 MBytes of GDDR3 SDRAM

[Video Card]

Video Card:	**ATI RADEON HD 3850 256MB GDDR3**
Video Bus:	PCIe v2.0 x16 (2.5 Gb/s) @ x16 (2.5 Gb/s)
Video BIOS Version:	010.067.000.000.026906

[Performance]

Processor Clock:	334.1 MHz
Memory Clock:	414.0 MHz (Effective 828.0 MHz)
Memory Bus Width:	256-bit
Number Of Unified Shaders:	320

Monitor

SAMSUNG SyncMaster 2494HM/2494HS (Digital)

[General information]

Monitor Name:	**SAMSUNG SyncMaster 2494HM/2494HS (Digital)**
Serial Number:	H9XS817474
Date Of Manufacture:	Week: 35, Year: 2009
Max. Vertical Size:	30 cm
Max. Horizontal Size:	53 cm
Horizontal Frequency:	30 - 81 kHz
Vertical Frequency:	56 - 60 Hz
Maximum Pixel Clock:	170 MHz

[Advanced parameters]

Input Signal:	Digital
Display Type:	RGB color
Gamma Factor:	2.20

[DPMS Modes]

Standby:	Not Supported
Suspend:	Not Supported
Active Off:	Supported
Standard Colour Space:	Not Supported
Preferred Timing Mode:	Supported
Default GTF Supported:	Not Supported
DFP 1.x Compatible:	No

[Supported Video Modes]

1280 x 800	60 Hz
1280 x 960	60 Hz
1280 x 1024	60 Hz
1440 x 900	60 Hz
1600 x 1200	60 Hz
1680 x 1050	60 Hz
1920 x 1080	531 x 298 mm, Pixel Clock 138.50 MHz

Drives

Floppy Drives

(S)ATA/ATAPI Drives

SAMSUNG HD501LJ

[General Information]

Drive Controller:	Serial ATA 3Gb/s
Drive Model:	**SAMSUNG HD501LJ**
Drive Revision:	CR100-11
Drive Serial Number:	S0ZFJ1MP806005
Drive Capacity:	**476,940 MBytes (500 GB)**
Drive Capacity [MB]:	**476940**

[Drive Geometry]

Number of Cylinders:	16383
Number of Heads:	16
Sectors Per Track:	63
Bytes Per Sector:	554
Bytes Per Track:	34902
Number Of ECC Bytes:	4
Number of Sectors:	16514064
Total 32-bit LBA Sectors:	268435455
Total 48-bit LBA Sectors:	976773168
Cache Buffer Size:	16384 KBytes
Controller Type:	Dual Ported, Multiple Sector Buffer, Read Cache

[Transfer Modes]

Sectors Per Interrupt:	Total: 16, Active: 16
Max. PIO Transfer Mode:	4
Multiword DMA Mode:	Total: 2, Active: -
Singleword DMA Mode:	Total: -, Active: -
Ultra-DMA Mode:	Total: 7 (SATA), Active: 6 (ATA-133)
Max. Multiword DMA Transfer Rate:	16.7 MBytes/s
Max. PIO with IORDY Transfer Rate:	16.7 MBytes/s
Max. PIO w/o IORDY Transfer Rate:	16.7 MBytes/s
Transfer Width:	16-bit
Native Command Queuing:	Supported, Max. Depth: 32
TRIM Command:	Not Supported

[Device flags]

Fixed Drive:	Present
Removable Drive:	Not Present
Magnetic Storage:	Present
LBA Mode:	Supported
DMA Mode:	Supported
IORDY:	Supported
IORDY Disableable:	Supported

[Features]

Write Cache:	Present, Active
S.M.A.R.T. Feature:	Present, Active
Security Feature:	Present, Inactive
Removable Media Feature:	Not Present, Disabled
Power Management:	Present, Active
Advanced Power Management:	Not Present, Inactive
Packet Interface:	Not Present, Disabled
Look-Ahead Buffer:	Present, Active
Host Protected Area:	Present, Enabled

Power-Up In Standby:	Not Suppported, Inactive
Automatic Acoustic Management:	Supported, Active
48-bit LBA:	Supported, Active

[Self-Monitoring, Analysis and Reporting Technology]

Raw Read Error Rate:	100/51, Worst: 100
Spin Up Time:	100/15, Worst: 100 (Data = 7104)
Start/Stop Count:	99/Always OK, Worst: 99 (Data = 1106)
Reallocated Sector Count:	253/10, Worst: 253
Seek Error Rate:	253/Always OK, Worst: 253
Seek Time Performance:	253/Always OK, Worst: 253
Power-On Hours/Cycle Count:	100/Always OK, Worst: 100 (Data = 1825)
Spin Retry Count:	253/Always OK, Worst: 253
Calibration Retry Count:	253/Always OK, Worst: 253
Power Cycle Count:	100/Always OK, Worst: 100 (Data = 809)
Soft Read Error Rate:	100/Always OK, Worst: 100 (Data = 1481876)
Reported Uncorrectable Errors:	89/Always OK, Worst: 89 (Data = 12)
Command Timeout:	253/Always OK, Worst: 253
Airflow Temperature / Exceed Count:	77/Always OK, Worst: 53 (Data = 23.0 °C)
Temperature	169/Always OK, Worst: 97 (Data = 23.0 °C)
Hardware ECC Recovered:	100/Always OK, Worst: 100 (Data = 1481876)
Reallocation Event Count:	253/Always OK, Worst: 253
Current Pending Sector Count:	100/Always OK, Worst: 100 (Data = 1)
Off-Line Uncorrectable Sector Count:	253/Always OK, Worst: 253
UltraDMA/SATA CRC Error Rate:	200/Always OK, Worst: 200
Write Error Rate:	253/Always OK, Worst: 100
Soft Read Error Rate:	253/Always OK, Worst: 100
Data Address Mark Errors	253/Always OK, Worst: 253

TSSTcorp CDDVDW SH-S203N

[General information]

Drive Model:	**TSSTcorp CDDVDW SH-S203N**
Drive Revision:	SB01
Serial Number:	SH-S203NFirmware
Device Type:	DVD+R DL

[Device capabilities]

Drive can read:	CD-R, CD-RW, DVD-R, DVD-RW, DVD+R, DVD+RW, DVD-RAM, DVD+R DL, MRW
Drive can write:	CD-R, CD-RW, DVD-R, DVD-RW, DVD+R, DVD+RW, DVD-RAM, DVD+R DL, MRW

Audio

nVIDIA MCP55 - High Definition Audio Controller

Audio Adapter:	**nVIDIA MCP55 - High Definition Audio Controller**
High Definition Audio Codec:	RealTek ALC883
HDA Codec ID:	10EC0883

ATI RV660/RV670 - High Definition Audio Controller

Audio Adapter:	**ATI RV660/RV670 - High Definition Audio Controller**
High Definition Audio Codec:	ATi RADEON HDMI
HDA Codec ID:	1002AA01

Network

NVIDIA nForce Networking Controller

[General information]

Network Card:	**NVIDIA nForce Networking Controller**
Vendor Description:	**NVIDIA nForce MCP Networking Adapter Driver**
MAC Address:	00-16-17-D6-60-DB

[Capabilities]

Maximum Link Speed:	1000 Mbps
Transmit Buffer Size:	775168 Bytes
Receive Buffer Size:	6201344 Bytes

NVIDIA nForce Networking Controller

[General information]

Network Card:	**NVIDIA nForce Networking Controller**
Vendor Description:	**NVIDIA nForce MCP Networking Adapter Driver**
MAC Address:	00-16-17-D5-C1-B9

[Capabilities]

Maximum Link Speed:	1000 Mbps
Transmit Buffer Size:	775168 Bytes
Receive Buffer Size:	6201344 Bytes

Ports

Serial Ports