

Vor

OC Tweaker\DRAM Timing Configuration

Primary Timing

CAS# Latency (tCL)	18	16
RAS# to CAS# Delay to Read (tRCRD)	18	18
RAS# to CAS# Delay to Write (tRCWR)	18	18
Row Precharge Time (tRP)	18	18
RAS# Active Time (tRAS)	38	38

Secondary Timing

Fail Count		3
RAS# Cycle Time (tRC)	56	56
RAS to RAS Delay (tRRD_S)	5	5
RAS to RAS Delay (tRRD_L)	7	7
Four Activate Window (tFAW)	34	34
Write to Read Delay (tWTR_S)	4	Auto
Write to Read Delay (tWTR_L)	12	Auto

Description: The time between sending a column address to the memory and the beginning of the data in response.
Min: 8
Max: 33

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Nach BIOS update

OC Tweaker\DRAM Timing Configuration

Primary Timing

CAS# Latency (tCL)	22	16
RAS# to CAS# Delay to Read (tRCRD)	22	18
RAS# to CAS# Delay to Write (tRCWR)	22	18
Row Precharge Time (tRP)	22	18
RAS# Active Time (tRAS)	52	38

Secondary Timing

Fail Count		3
RAS# Cycle Time (tRC)	74	56
RAS to RAS Delay (tRRD_S)	4	5
RAS to RAS Delay (tRRD_L)	8	7
Four Activate Window (tFAW)	34	34
Write to Read Delay (tWTR_S)	4	Auto
Write to Read Delay (tWTR_L)	12	Auto

Description: The time between sending a column address to the memory and the beginning of the data in response.
Min: 8
Max: 33

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Write Recovery Time (tWR)	24	Auto
Trcpage (tMAW.MAC)	0	Auto
TrdrdScL	5	Auto
TwrwrScL	5	Auto
Refresh Cycle Time (tRFC)	560	560
Refresh Cycle Time 2 (tRFC2)	416	416
Refresh Cycle Time 4 (tRFC4)	256	256

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Write Recovery Time (tWR)	24	Auto	Max: 12
Trcpage (tMAW.MAC)	0	Auto	
TrdrdScL	5	Auto	
TwrwrScL	5	Auto	
Refresh Cycle Time (tRFC)	560	560	
Refresh Cycle Time 2 (tRFC2)	416	416	
Refresh Cycle Time 4 (tRFC4)	256	256	
CAS Write Latency (tCWL)	18	Auto	

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CAS Write Latency (tCWL)	16	Auto	Min: 2 Max: 14
Read to Precharge (tRTP)	12	Auto	
Trdwr	7	Auto	
Twrrd	3	Auto	
TwrwrSc	1	Auto	
TwrwrSd	6	Auto	
TwrwrDd	6	Auto	
TrdrdSc	1	Auto	

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Read to Precharge (tRTP)	12	Auto
Trdwr	11	Auto
Twrrd	1	Auto
TwrwrSc	1	Auto
TwrwrSd	6	Auto
TwrwrDd	6	Auto
TrdrdSc	1	Auto
TrdrdSd	4	Auto
TrdrdDd	4	Auto
tCKE	9	Auto
ProcODT		Auto
Command Rate (CR)	1	Auto
Gear Down Mode		Auto

Description: The 2x Fine Gran Refresh command
Min: 60
Max: 990

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TrdrdSd	4	Auto
TrdrdDd	4	Auto
tCKE	9	Auto
ProcODT		Auto
Command Rate (CR)	1	Auto
Gear Down Mode		Auto
Power Down Enable		Disabled
Data Bus Configuration		